WHAT IS CLAIMED IS:

| 1 | | 1. | A method for processing a multi-carrier signal transmitted across | | |
|--|--|--|---|--|--|
| 2 | a channel, comprising | | | | |
| | | | | | |
| 3 | | receiv | ring the multi-carrier signal in time domain; | | |
| 4 | | estimating a channel transfer function using a subset of the multi-carrier | | | |
| 5 | signal in time domain; | | | | |
| 5 | | transf | Forming the multi-carrier signal from time-domain into frequency | | |
| 7 | domain; and | | | | |
| | compensating for the channel transfer function using the estimated | | | | |
| | channel transfer function. | | | | |
| E | | 2. | The method of claim 1 wherein the subset of the multi-carrier | | |
| | signal in time domain comprises training symbols. | | | | |
| THE THE PARTY OF T | | 3. | The method of claim 2 wherein the estimating step comprises | | |
| 2 | performing a convolution of the training sequence. | | | | |
| 1 | | 4. | The method of claim 3 wherein the estimating step further | | |
| 2 | comprises processing a weighing matrix in time domain. | | | | |
| ı | | 5. | The method of claim 4 wherein the processing of the weighing | | |
| 2 | matrix comp | rises pe | erforming a multiplication of the weighing matrix with the | | |
| 3 | convolved training sequence. | | | | |
| | | | | | |
| l | | 6. | The method of claim 5 wherein the weighing matrix comprises | | |
| 2 | values that account for the finite time response of the channel and the position of zero | | | | |
| 3 | sub-carriers in the frequency domain. | | | | |
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A method for communicating data between a transmitter and a

impulse response of the channel.

13.

receiver separated by a channel, the method comprising:

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| 3 | at the transmitter end: | | | | | |
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| 4 | generating a plurality of modulated sub-carrier signals based on the data; | | | | | |
| 5 | transforming the plurality of modulated sub-carrier signals into a | | | | | |
| 6 | plurality of time-domain signals; | | | | | |
| 7 | transmitting the plurality of time-domain signals across the channel; and | | | | | |
| 8 | at the receiver end: | | | | | |
| 9 | receiving the multi-carrier signal in time domain; | | | | | |
| 0 | estimating a channel transfer function using a subset of the multi- | | | | | |
| 1 | carrier signal in time domain; | | | | | |
| 2 | transforming the multi-carrier signal from time-domain into | | | | | |
| 3 | frequency domain; and | | | | | |
| 7 | compensating for the channel transfer function using the | | | | | |
| | estimated channel transfer function. | | | | | |
| 1 | 14. The method of claim 13 the estimating comprises performing a | | | | | |
| 2 | cyclic convolution on a training sequence embedded in the subset of the multi-carrier | | | | | |
| 3 | signal in time domain. | | | | | |
| di di | | | | | | |
| 1 | 15. The method of claim 14 wherein the estimating further comprises | | | | | |
| 2 | multiplying a weighing matrix with the convolved training sequence. | | | | | |
| 1 | 16. The method of claim 15 wherein the step of multiplying occurs at | | | | | |
| 2 | a window of time during which the multi-carrier signal has optimum energy. | | | | | |
| _ | | | | | | |
| 1 | 17. In a multi-carrier data communication system, a receiver | | | | | |
| 2 | comprising: | | | | | |
| 3 | a channel estimator that receives a multi-carrier time-domain signal at | | | | | |
| 4 | an input and generates a plurality of channel estimates at an output; | | | | | |

The receiver of claim 17 wherein the channel estimator 18 comprises:

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a correlator coupled to receive a training sequence embedded in the multi-carrier time-domain signal, and configured to perform a convolution operation on the training sequence; and

a multiplier coupled to the correlator and configured to multiply a channel estimation weighing matrix with an output of the correlator.

- The receiver of claim 18 wherein the channel estimation 19 weighing matrix comprises values that account for the finite time response of the channel and the position of zero sub-carriers in the frequency domain.
- COMPAND OF CONTRACT 20. The receiver of claim 19 wherein the estimator further comprises a timing circuit coupled to the correlator and the multiplier, and configured to extract an optimum time for the multiplication performed by the multiplier.
- 21. The receiver of claim 20 wherein the correlator comprises a 1 2 matched filter that performs a cyclic convolution.
 - 22. The receiver of claim 21 wherein the matched filter is also configured to acquire timing of received signal for synchronization purposes.

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| | 23. | The receiver of claim 20 wherein the estimator further comprises |
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| a memory un | it coupl | ed to the correlator and configured to store the output of the |
| correlator. | | |

- 24. The receiver of claim 23 wherein the estimator further comprises: a delay unit having an input coupled to the input of the channel estimator and an output; and a multiplexer having a first input coupled to the output of the delay unit, a second input coupled to an output of the multiplier, a control input and an output,
- wherein, the multiplexer is configured to combine a payload portion of the multi-carrier time-domain signal with the plurality of channel estimates.
- The receiver of claim 25 wherein the time-domain to frequencydomain transform unit is configured to perform a fast Fourier transform function.
 - A multi-carrier data communication system comprising: a transmitter including:
- a demodulator/deserializer configured to convert an input data stream into a parallel plurality of multi-carrier signals;
- a frequency-domain to time-domain converter having an input coupled to the modulator/deserializer and configured to transform the parallel plurality of multi-carrier signals from frequency domain into time domain at an output;
- a guard period insertion block coupled to the frequency-domain to timedomain converter and configured to insert a guard period in the output of the frequency-domain to time-domain converter;
- a serializer coupled to an output of the guard period insertion block and configured to perform a parallel to serial conversion on the signal; and
- a digital-to-analog converter coupled to the serializer and configured to convert the digital signal into an analog signal and to transmit the analog multi-carrier time-domain signal across a channel:

| 16 | a receiver including: | | | | | |
|--|---|--|--|--|--|--|
| 17 | an analog-to-digital converter coupled to receive the analog | | | | | |
| 18 | signal and configured to convert the analog signal into a digital signal; | | | | | |
| 19 | a deserializer coupled to the analog-to-digital converter and | | | | | |
| 20 | configured to convert the digital signal into a plurality of parallel signals; | | | | | |
| 21 | a channel estimator coupled to the deserializer and configured to | | | | | |
| 22 | derive channel estimates using a training sequence embedded into to received time- | | | | | |
| 23 | domain signal; | | | | | |
| 24 | a guard period removal block coupled to an output of the channel | | | | | |
| 25 | estimator and configured to remove the guard period; | | | | | |
| 26 | a time-domain to frequency-domain converter coupled to an | | | | | |
| 273 | output of the guard period removal block; | | | | | |
| | an equalizer coupled to the time-domain to frequency-domain | | | | | |
| 28 29 30 31 31 31 31 31 | converter and configured to equalize the signal using the channel estimates; | | | | | |
| 304 | a serializer/demodulator coupled to an output of the equalizer and | | | | | |
| 3) [©] | configured to generate an output data stream. | | | | | |
| Park San | | | | | | |
| H | 27. The data communication system of claim 26 wherein the channel | | | | | |
| 20 | estimator comprises: | | | | | |
| 3 | a correlator coupled to receive a training sequence embedded in the | | | | | |
| 4 | multi-carrier time-domain signal, and configured to perform a convolution operation | | | | | |
| 5 | on the training sequence; and | | | | | |
| 6 | a multiplier coupled to the correlator and configured to multiply a | | | | | |
| 7 | channel estimation weighing matrix with an output of the correlator. | | | | | |
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| 1 | 28. The receiver of claim 27 wherein the channel estimator further | | | | | |
| 2 | comprises a timing circuit coupled to the correlator and the multiplier, and configured | | | | | |

29. The receiver of claim 28 wherein the correlator comprises a matched filter that performs a cyclic convolution.

to extract an optimum time for the multiplication performed by the multiplier.

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